## Sample Protection Philosophy for Non-Injecting Inverter-Based Sources



This document is not an approval for connection. It is intended as a guide for proponents regarding the kinds of protections needed. This document is a summary of a sample protection philosophy for non-injecting, inverterbased (NI/I) connections including storage, solar and wind. It provides guidance to a Distributed Energy Resource (DER) proponent on good utility practice as it relates to protection requirements of NI/I DERs.

A proponent will need to submit detailed protection settings after the utility has completed the impact assessment for the connection application submitted.

The protection system of the connection will be designed to:

- Detect internal faults with the generator facility, downstream of the Point of Common Coupling (PCC), and automatically disconnect the NI/I source
- Detect external faults on the utility feeder and automatically disconnect the NI/I source
- Detect islanding conditions and disconnect the NI/I source
- Detect export of power from the NI/I source to the utility feeder and automatically disconnect the NI/I source

## Internal faults within the generator facility

The following protections are in place to protect against internal faults resulting from the NI/I source:

**Multi-Function Relay** — At the PCC, a multi-function relay will be installed to monitor internal faults resulting from the NI/I source. The 52 Trip Breaker will trip if it detects the following:

- 25 Synchronization Check 810/U Under-Frequency and Over-Frequency
- 27 Under-Voltage

• ID - Active Anti-Islanding

• 59 - Over-Voltage

**Inverter Breakers** — Each inverter is equipped with an AC breaker at the output of the inverter providing additional over-current protection.

**Facility Over-Current Protection** — All circuits within the facility are protected from both phase-to-phase and phase-to-ground faults by appropriate over-current protection devices. Fuses are sized to clear under fault conditions within the generator facility.

## External phase and ground faults in the distribution system

The following protections are in place to protect against external faults resulting from the utility feeder:

**Multi-Function Relay** — At the main utility service, prior to the first facility load, a multi-function relay will be installed to monitor faults from the utility feeder. The 52 Trip Breaker at the NI/I source PCC will trip under the following faults:

- 27 Under-Voltage 59 Over-Voltage
- 32R Reverse Power
   810/U Under-Frequency and Over-Frequency
- 50/51 Over-Current 67 Directional

**Inverter Protection** — The inverters proposed for this project are certified to UL 1741, IEEE 1547, CSA C22.2 107.1-01 standards and will behave accordingly.

## Anti-islanding

**Energy Resource Facility** – This will operate in a grid-following mode and will not operate islanded.

**Anti-Islanding Inverters** — The NI/I source inverters contain both passive and active anti-islanding protection as required by IEEE 1547 and UL1741 SA. If the utility's normal power supply is interrupted, the inverters detect the loss of power and disconnect.

#### **Reverse power**

**Reverse Power Protection** — In addition to the multi-function relay at the utility supply monitoring reverse power (32R), the load is continually monitored to ensure the NI/I source discharge is below the consumption of the facility. This also protects against power injection to the utility grid.

#### **Directional over-current**

**Directional Over-Current Protection** — Directional over-current relays are normally used on incoming line circuit breakers on buses which have two or more sources. They are connected to trip an incoming line breaker for fault current flow back into the source, so that a fault on one source is not fed by the other sources.

### Special comment regarding inverter-based generation

The inverters specified for this project have a limited fault current contribution.

• Since inverters are current-limited devices, unlike rotating generators, the fault current is very close to the maximum output current, limiting the fault current in the system to 120% – 140% of FLA

| Description                         | IEEE Device | Internal Faults | External Faults | Anti-Islanding | Reverse Powe |
|-------------------------------------|-------------|-----------------|-----------------|----------------|--------------|
| Over-Voltage                        | 59          | x               | x               | x              |              |
| Under-Voltage                       | 27          | x               | x               | х              |              |
| Over-Frequency                      | 810         | x               | x               | х              |              |
| Under-Frequency                     | 81U         | x               | x               | x              |              |
| Instantaneous<br>Over-Current Phase | 50          | x               | x               |                |              |
| Timed Over-Current<br>Phase         | 51          | x               | x               |                |              |
| Reverse Power                       | 32R         |                 |                 | x              | x            |
| Directional                         | 67          | x               | x               |                |              |
| Active Anti-Islanding               | IEEE 1547   |                 |                 | x              |              |

## Table 1: Protection summary matrix

X = Primary Y = Secondary

# Table 2: Protection elements

| Protection Element<br>Function | Device Number | Feeder Protection<br>Relay/Shunt Trip | IEEE 1741 SA Inverter |
|--------------------------------|---------------|---------------------------------------|-----------------------|
| Over-Voltage                   | 59            | x                                     | Y                     |
| Under-Voltage                  | 27            | x                                     | Y                     |
| Over-Frequency                 | 810           | x                                     | Y                     |
| Under-Frequency                | 81U           | x                                     | Y                     |
| Synchronization Check          | 25            | x                                     | Y                     |
| Reverse Power                  | 32R           | х                                     |                       |
| Over-Current                   | 50/51         | x                                     | Y                     |
| Directional                    | 67            | x                                     |                       |
| Active Anti-Islanding          | ID            |                                       | x                     |

X = Primary Y = Secondary